

Fig.1

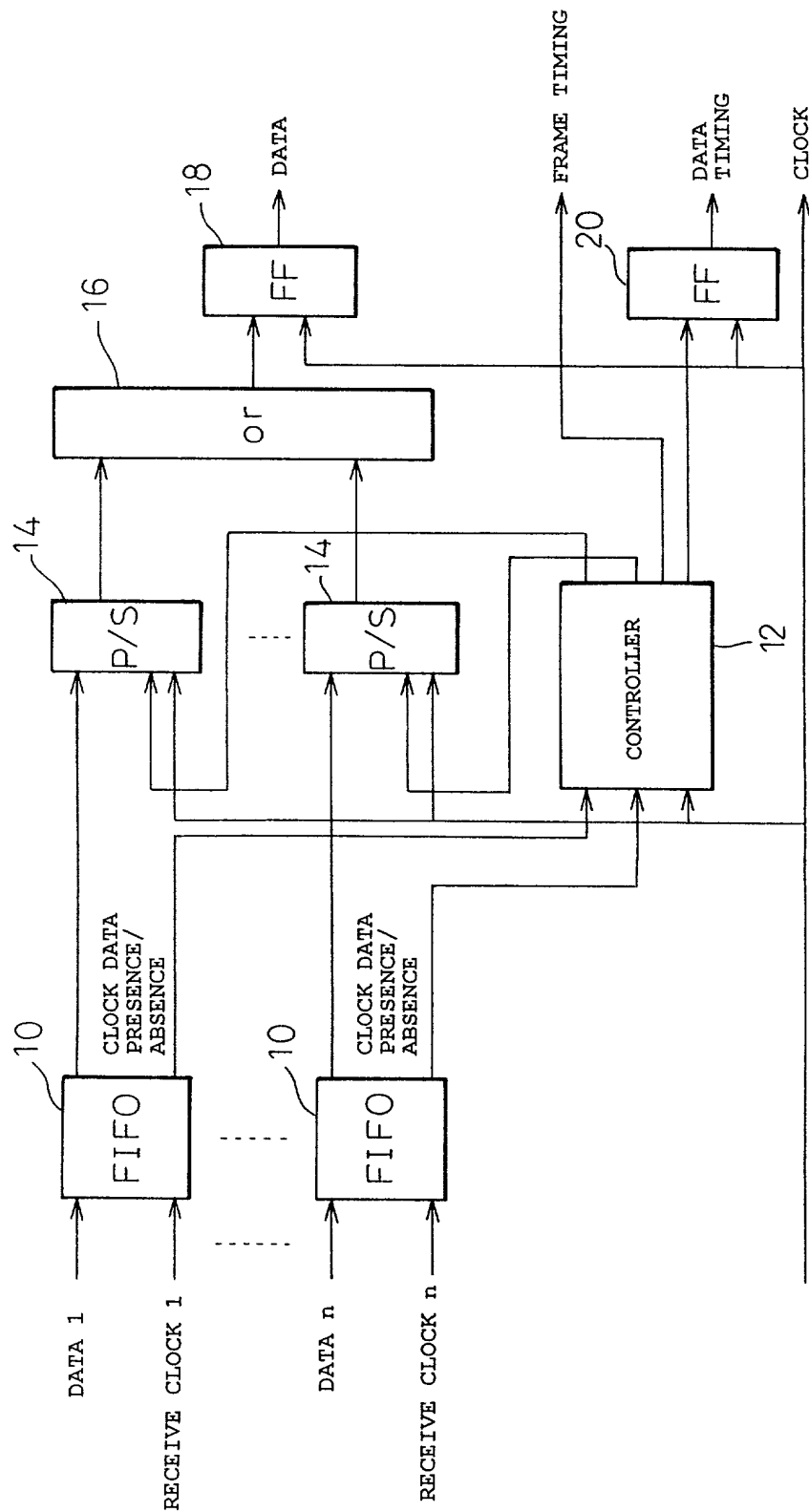


Fig.2

FIFO STATUS	DATA PRESENCE		DATA ABSENCE	
	FIRST BIT	SECOND BIT	FIRST BIT	SECOND BIT
DATA	1(0)	1(0)	×	×
DATA TIMING	0	1	0	0

Fig. 3

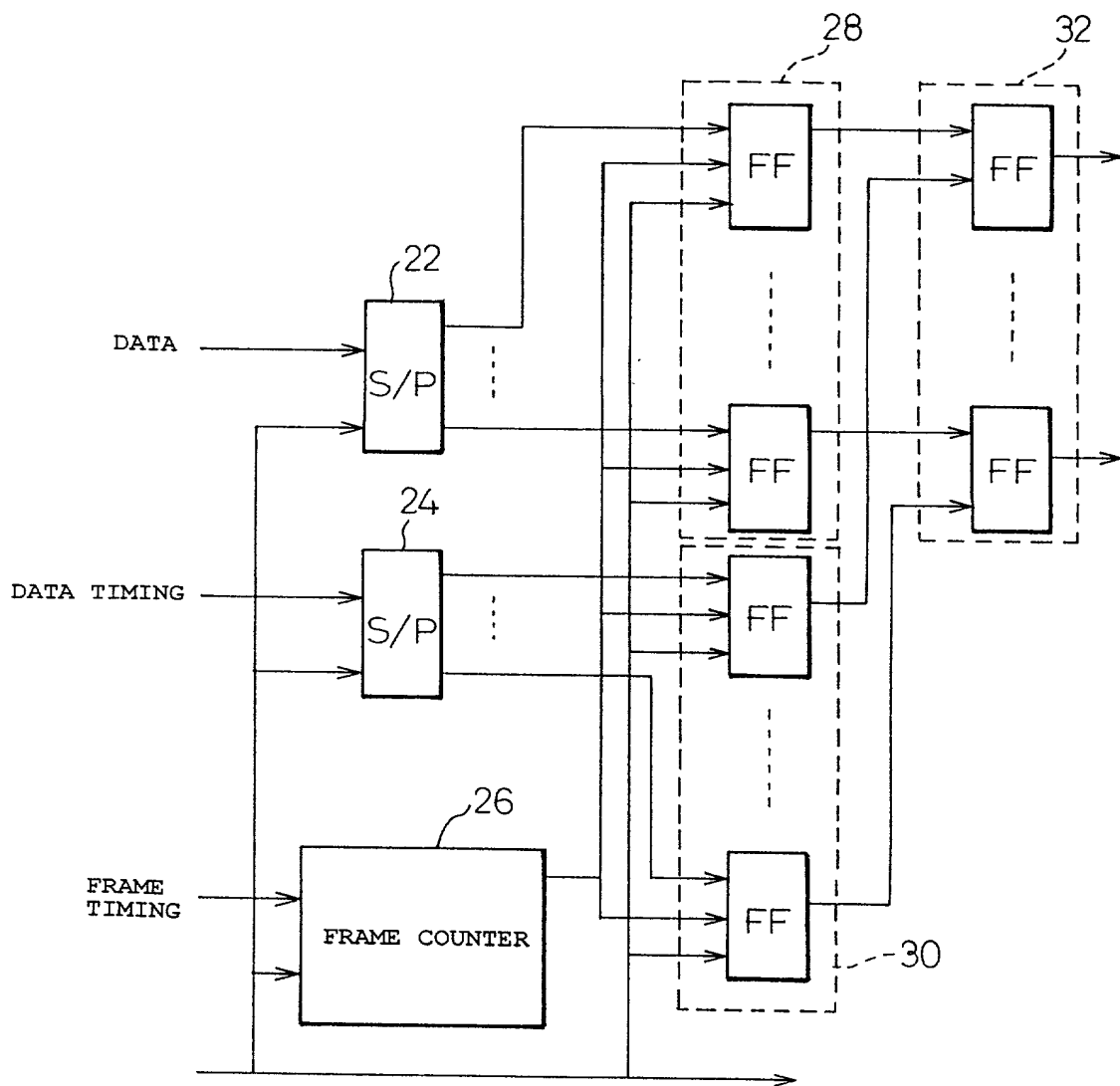


Fig.4

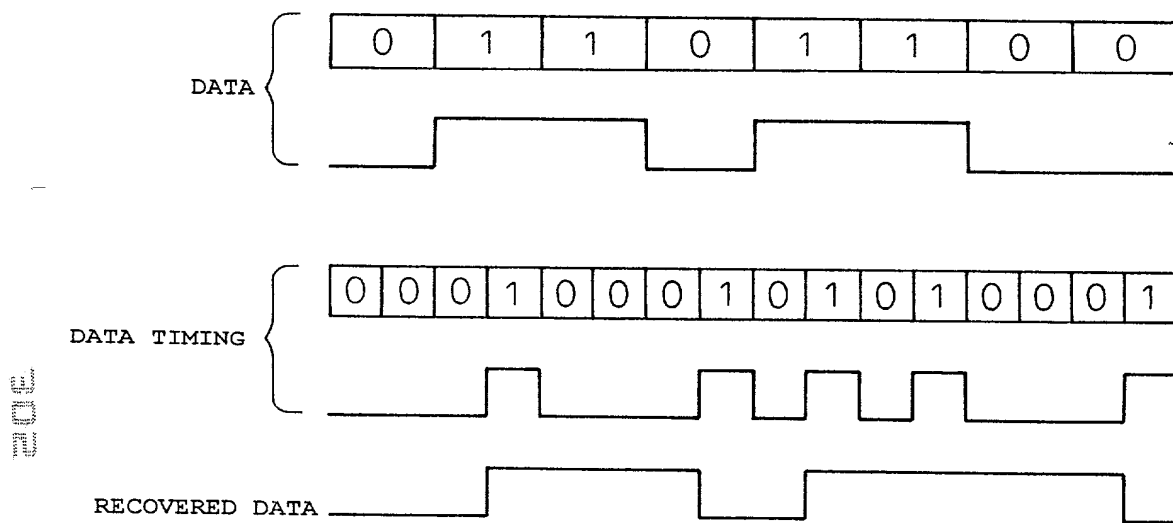


Fig. 5

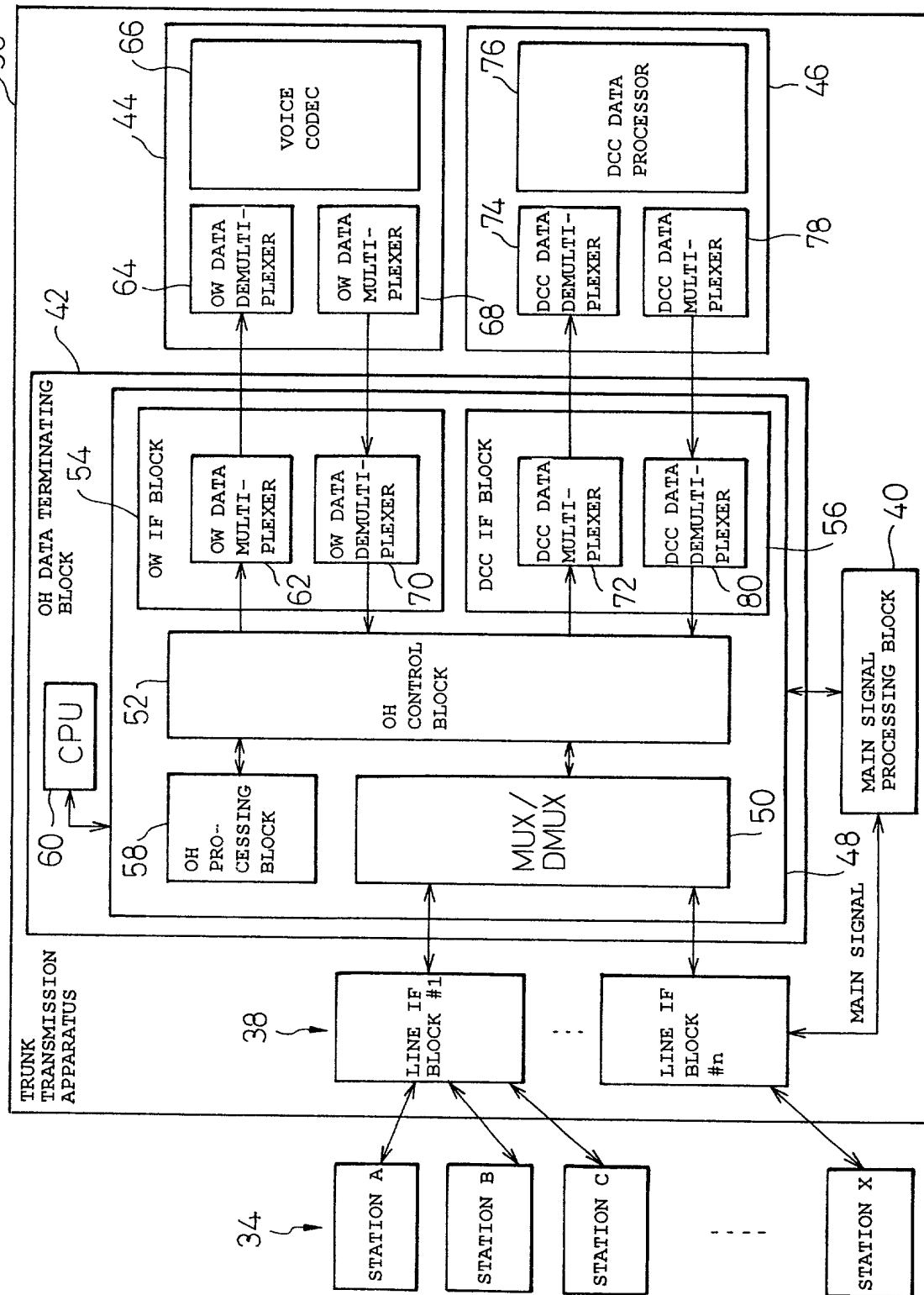


Fig. 6

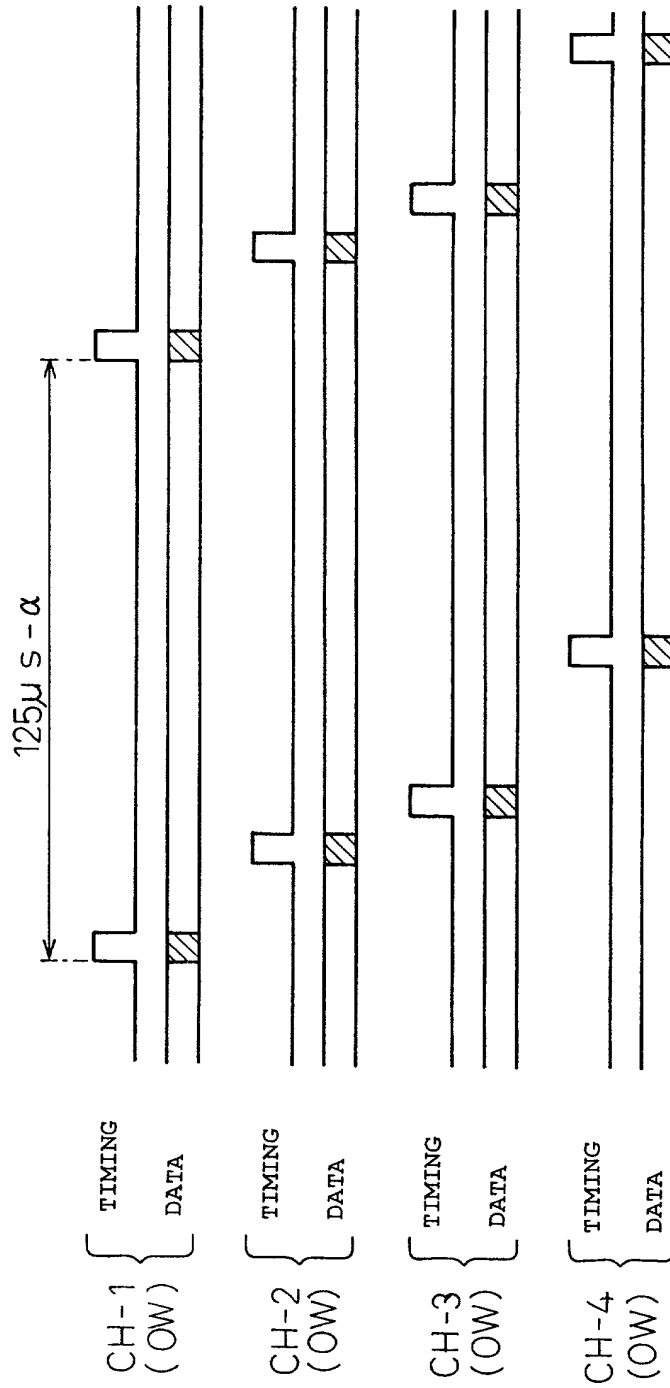


Fig. 7

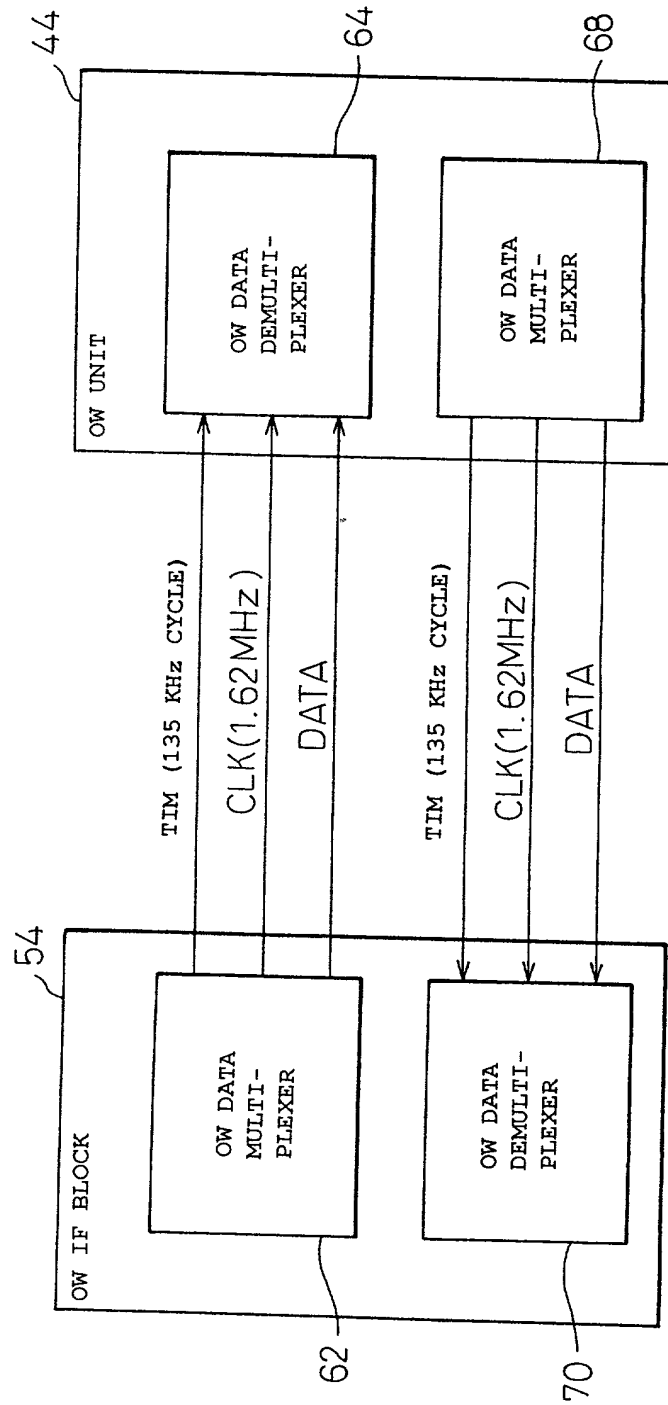


Fig. 8

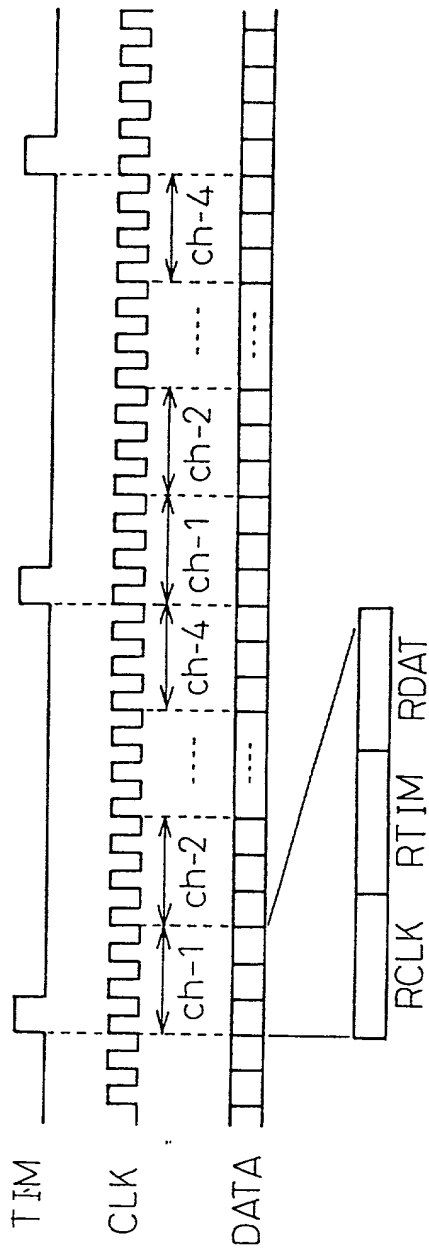


Fig. 9

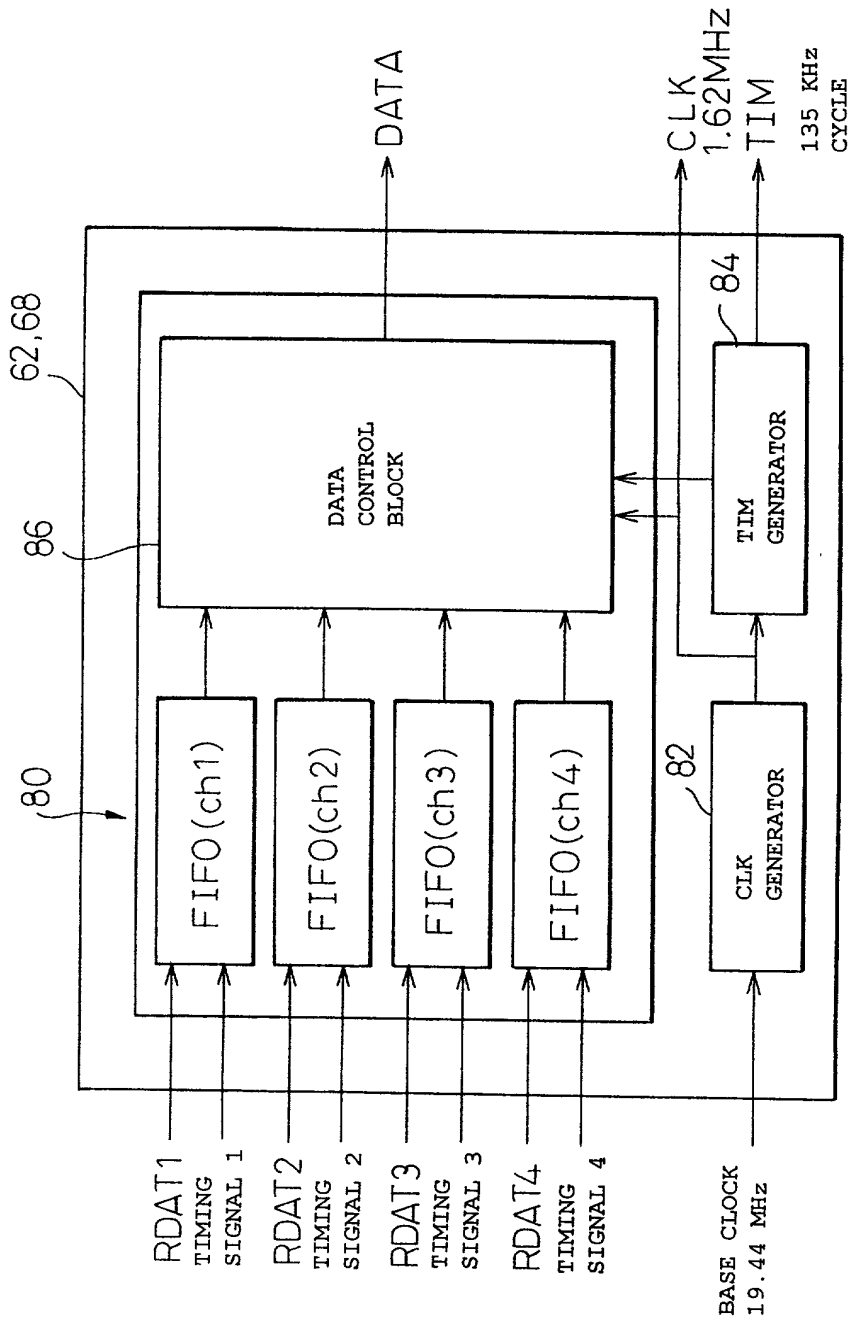


Fig.10

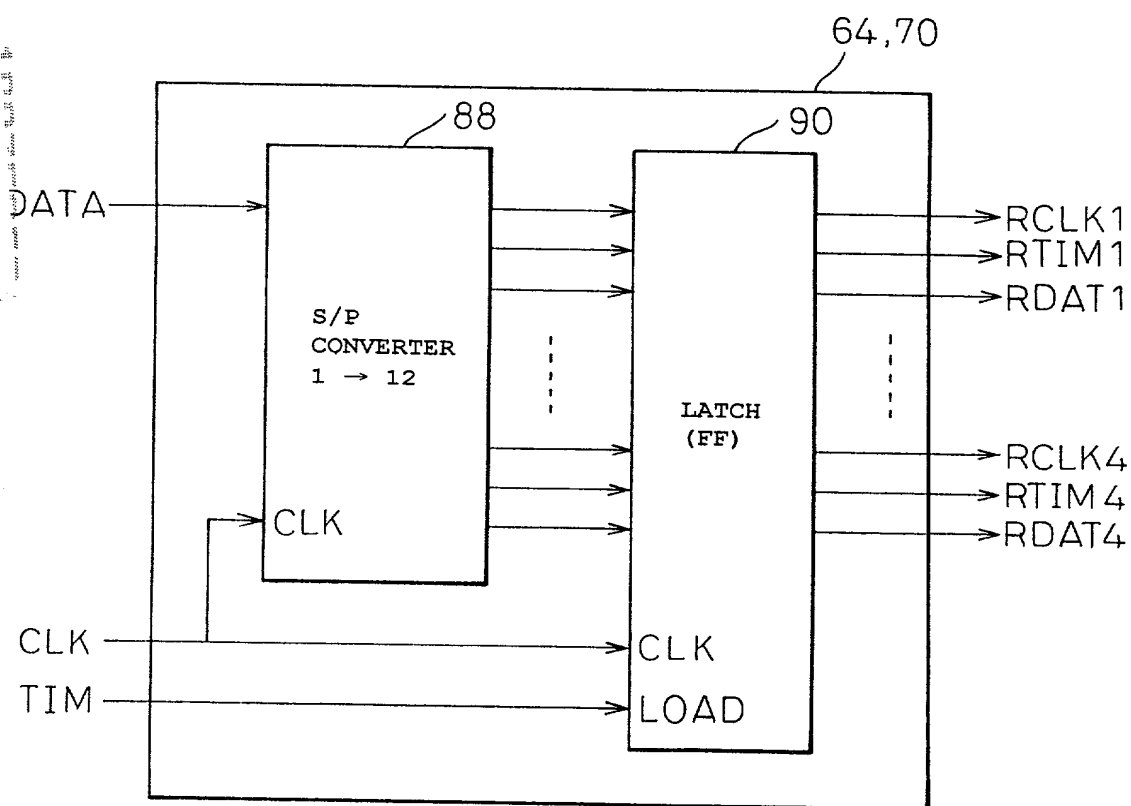


Fig.11

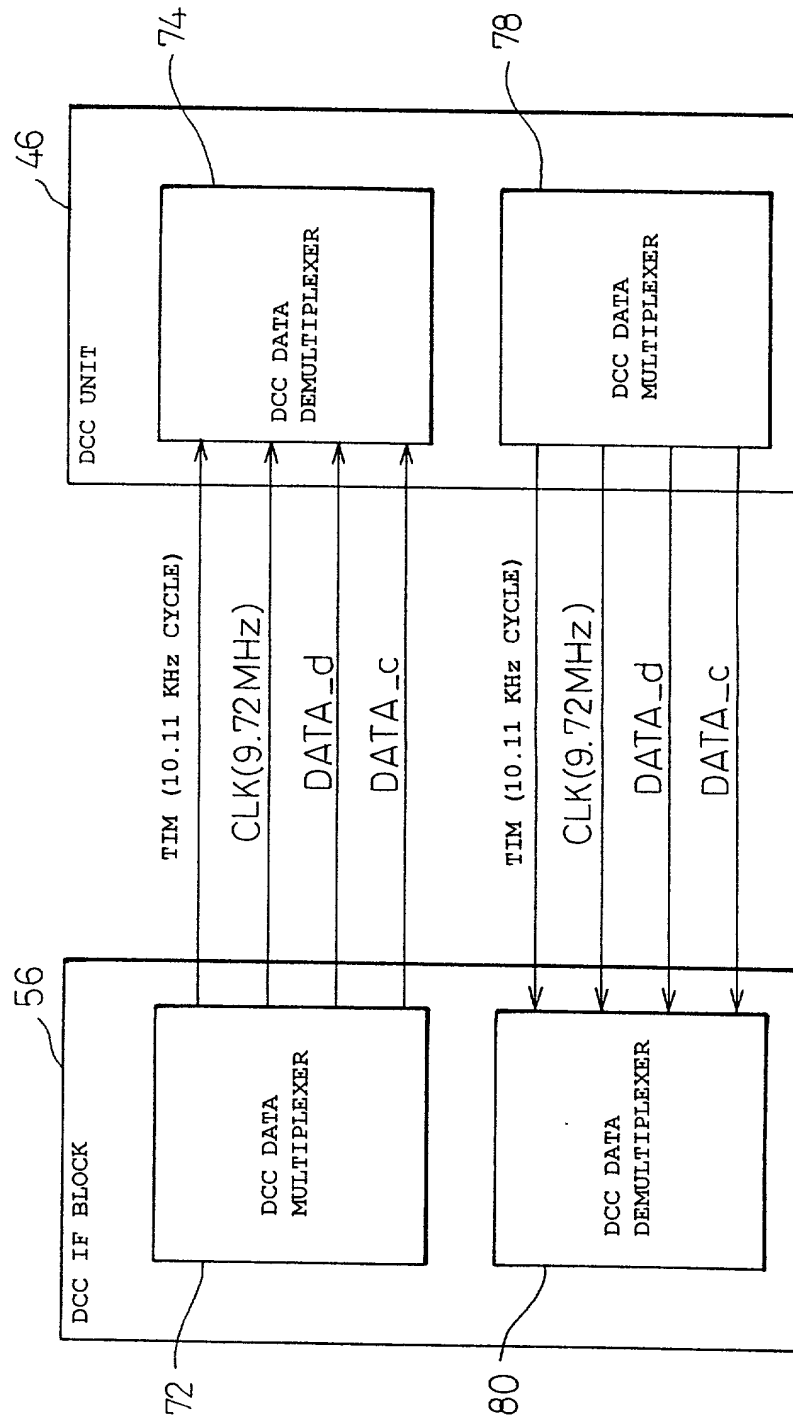


Fig.12

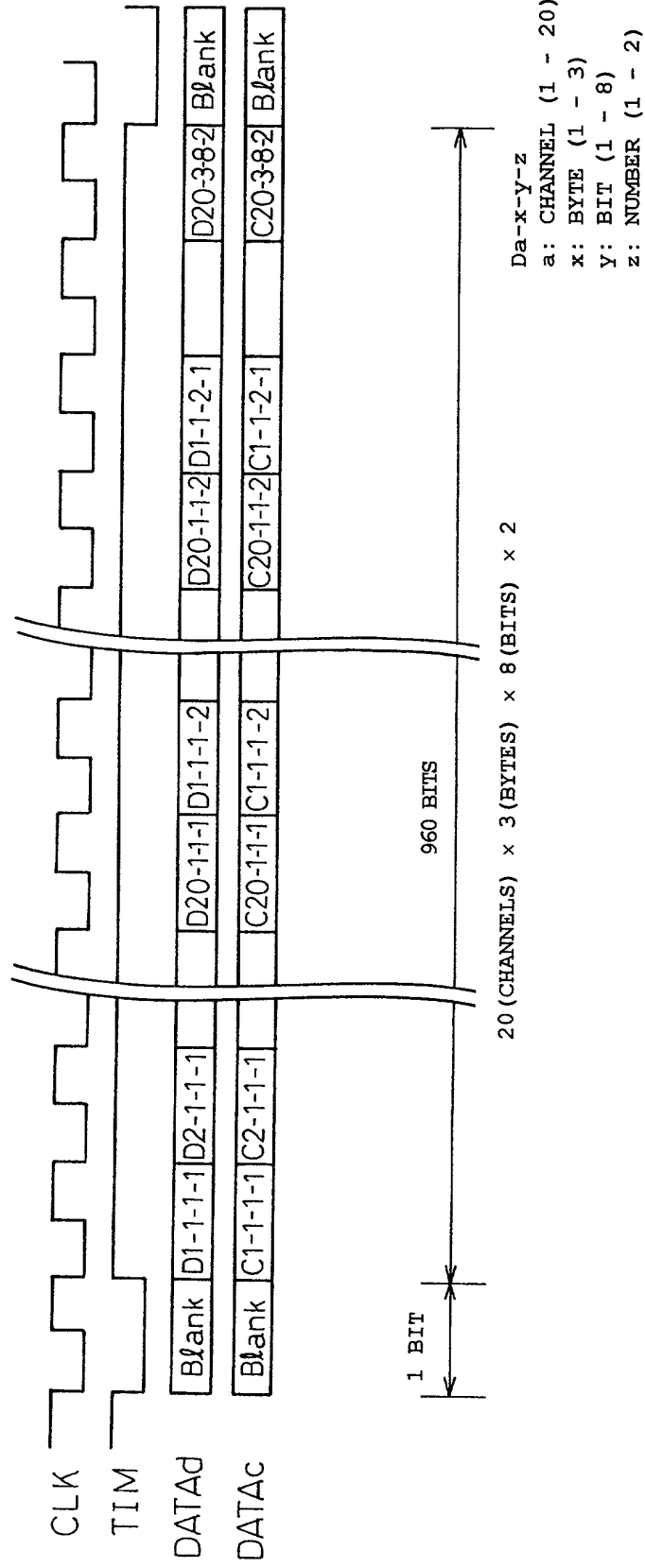


Fig.13

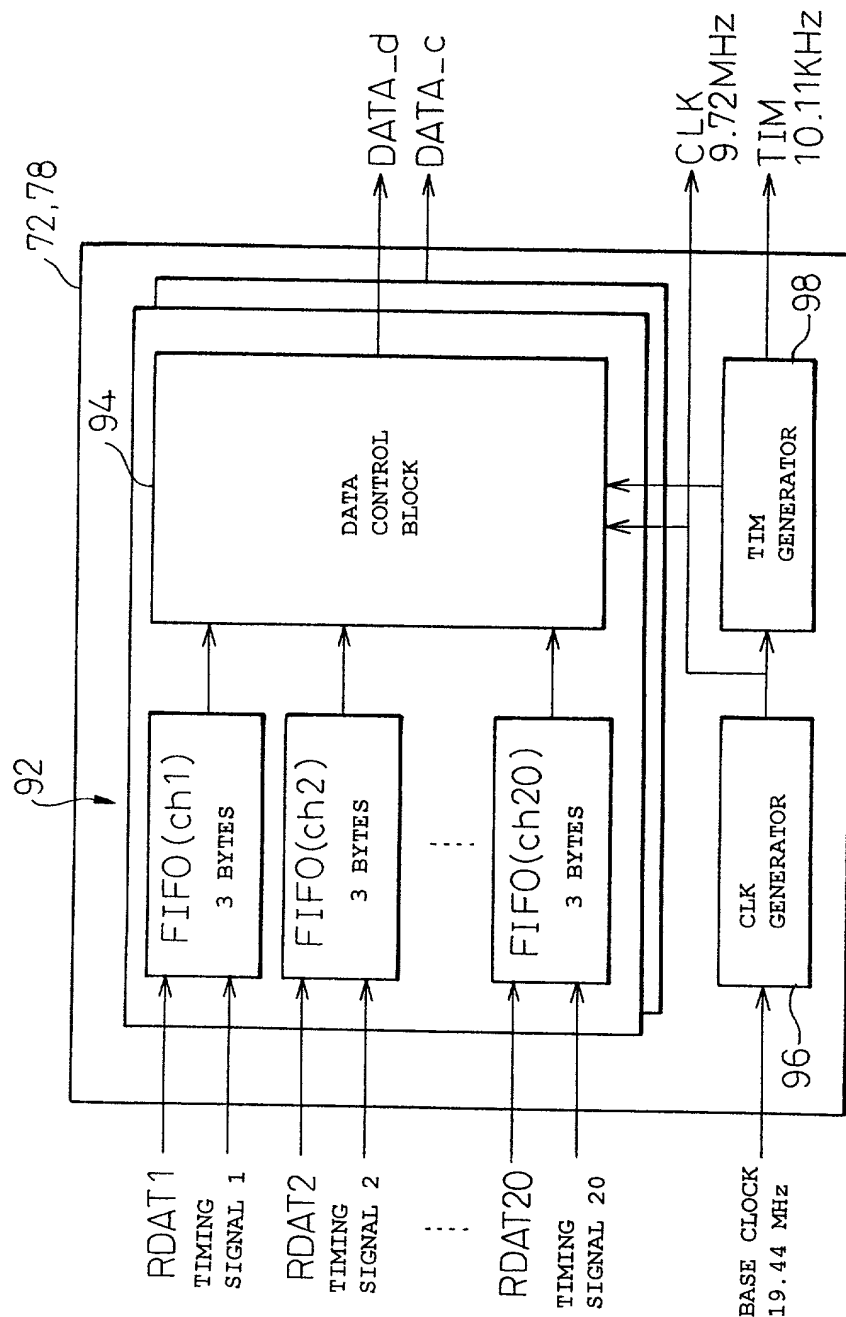


Fig.14

